

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of: Terry D. Little et al.

Serial No.: 09/747,257

Art Unit: 2637

Filed: December 22, 2000

Examiner: Tse, Y.

For: **LINEARIZED DIGITAL PHASE-LOCKED LOOP METHOD**

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

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Technology Center 2600

**SUBMISSION OF DRAWING AMENDMENT (37 CFR 1.121)**

Attached please find

(check applicable items)

☒ replacement drawing sheet(s) in permanent ink

☐ annotated marked-up copy of the amended drawing(s) with annotations showing the change(s).

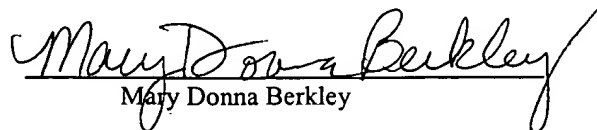
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Dated: October 26, 2004

**CERTIFICATE OF MAILING (37 CFR 1.8(a))**

I hereby certify that this paper (along with any paper referred to as being attached or enclosed) is being deposited with the United States Postal Service on the date shown below with sufficient postage as first class mail, in an envelope addressed to the Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.

  
Mary Donna Berkley

Date: October 26, 2004